Subject: Invitation to UIDAI IRIS authentication technology workshop

Dear All,

This is in continuation to EOI for Participation in Iris Authentication Device Ecosystem Development

(Reference Number: K-11020/16/2012-UIDAI), Iris Proof of Concept (PoC) study carried out and

subsequent IRIS Authentication Accuracy - PoC Report published on UIDAI's website.

UIDAI intends to carry out another round of PoC in December 2012. Towards preparing for the same,

UIDAI is holding a workshop for all IRIS device suppliers at UIDAI's Bangalore Technology Centre on 19th

Nov 2012.

All participants of Iris Authentication Device Ecosystem Development expression of interest as well as

other IRIS device suppliers interested in participating in this PoC may kindly participate in the workshop.

Interested IRIS device organisations may kindly send name and designation of respective representatives

who will be attending the workshop as a confirmation of participation. Due to space constraint, it is

requested that participation per supplier may be limited to two to three person only. The agenda for the

workshop is attached. The confirmation may be sent via email to the following by 10th Nov 2012.

Workshop date: 19th November 2012

Time: 930AM-5pm

Venue:

UIDAI Technology Center Planning Commision, Govt. of india, 1st floor office -11, Salarpuria Touchstone building Marathhalli, Sarjapur outer Ring Road

Bangalore-560 103

Landmark: Opposite new horizon engineering collage.

RSVP your participation before 10th November to auth.uidai@gmail.com

<u>Agenda</u>

<u>Sr.</u>	Time	Agenda	
No.			
1.	9.30 AM-10.00 AM	Registration	
2.	10:00 AM-11:00 AM	UIDAI Authentication Update / Review	
		recently concluded PoC	
3.	11.00 AM to 11.15 AM	TEA	
4.	11.15 AM to 12.15 PM	Feedback to IRIS device vendors and backend	
		SDK provider for improvements required	
5.	12.15 AM to 1.00 AM	Improvisation Requirement by device vendors	
		& SDK provider	
6.	1.00 PM to 2.00 PM	Lunch	
7.	2.00 PM to 3.00 PM	Approach for developing PoC design, client	
		applications and other backend readiness	
8.	3.00 PM to 4.00 PM	STQC - IRIS device specifications & certification	
		requirements	
9.	4.00 PM to 5.00 PM	Road ahead – Device certification methodology	
		, Timelines	